# An Approach to Drawing Circuit Diagrams for Text Books 

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#### Abstract

The development of a library of pictograms is described. The pictograms are defined using macros embodied in $\mathrm{P}_{\mathrm{T}} \mathrm{CT}_{\mathrm{E}} \mathrm{X}$ and used to create an application to enable high definition circuit diagrams to be easily included in $\mathrm{T}_{\mathrm{E}} \mathrm{Xdocuments}$.


## Introduction

The problems of including pictures within a $\mathrm{T}_{\mathrm{E}} \mathrm{X}$ document have been considered by several people e.g. Norris et al and recently by Rogers and by Childs et al. Norris imports a PostScript file, Roger's paper suggests creating a mini-graphics package while Childs argues for a specialised hardware. Textures which we use on Apple hardware provides a $\backslash$ special \{picture\#\} to be included which allows for such combinations of pictures and text to be created as


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This facility is excellent if mere representation is all that is required, but there is little flexibility once the picture is imported to $\mathrm{T}_{\mathrm{EX}}$ and making changes becomes tedious. We consider here a use of PTCTEX to create an application, referred to as $\mathrm{CIRT}_{\mathrm{I}} \mathrm{XX}$ for brevity, which might be used to produce electronic circuit diagrams. The use of $\mathrm{P}_{\mathrm{I}} \mathrm{CTE}_{\mathrm{E}} \mathrm{X}$ was considered to have the following advantages:

1. it maintains device independence
2. it allows a mixture of good graphic pictograms with the elegance of $\mathrm{T}_{\mathrm{E}} \mathrm{X}$ typesetting, ideal for circuit diagrams
3. compositors need only be conversant with $\mathrm{TEX}^{\mathrm{X}}$ and the simple process of creating diagrams embodied in $\mathrm{CI}_{\mathrm{I}} \mathrm{RTEX}$ to produce diagrams.

## Concepts Used in $\mathrm{CIRT}_{\mathrm{E}} \mathrm{X}$

The simple principle adopted is that small, recurring objects are defined in a co-ordinate system using

1 pt as the base dimension of the space. From these simple objects, small pictograms are defined and from these pictograms, complete circuit diagrams can be drawn. Thus

> and are used to make

Section 9.2 of The PICTEX $_{\text {I }}$ Manual discusses the use of \setdimensionmode to \put objects in an absolute space with respect to a given origin. However, by working consistently with 1 pt as the base dimension, co-ordinate space was eventually preferred. There are two possible approaches to defining the objects and pictograms, in particular:

1. to define the objects in terms of the absolute coefficients $(x, y)$ of the final diagram
2. to build up a box using co-ordinates relative to a given origin within the box and then to $\backslash$ put \{box\} at $x y$
where $(x, y)$ is now absolute within the final diagram.
In the final version of $\mathrm{CI}_{\mathrm{I}} \mathrm{TT}_{\mathrm{E}} \mathrm{X}$, both approaches are used. It was initially decided to relate all objects and pictograms to an origin at their bottom left hand corner, but eventually it was realised that it was convenient if the origin of some of them was logical rather than predefined in this way.

For example, consider a simple vertical rectangle used, according to BS3939, to represent a resistor.

$$
(x, y) \star \Vdash^{(x+6, y+18)}
$$

Using approach $\mathrm{A}, \backslash \mathrm{Rv}$ can be defined by
\def $\backslash \mathrm{Rv} \# 1 \# 2\{\%$ \#1=\$x $\$$,\#2=\$y\$
\cxone=\#1 \advance\cxone by 6
\cyone=\#2 \advance $\backslash$ cyone by 18
\putrectangle corners at
\{\#1\}\{cyone\} and \{cxone\}\{\#2\}\}

It is then used simply as $\backslash \operatorname{Rv}\{x\}\{y\}$ where $x, y$ are absolute coordinates in the space of the final diagram.

Approach B follows the suggestion made in Section 8.2 of the PICTEX manual on how to include pictures in pictures. Thus $\backslash \mathrm{Rv}$ can be defined by

```
\def \Rv{\%
\putrectangle corners at 0 18 and 6 0
}
```

$\backslash R v$ is subsequently placed in the final picture using $\backslash$ put $\{\backslash \operatorname{Rv}\}$ [bl] at $x y$. In this case, approach $B$ is preferred because it is simpler and use of \put allows for an $x_{s h i f t}$ and/or a $y_{s h i f t}$ so that the resistor can be located at the known end of a vertical line and offset 3pt to place the origin. Note that we found that the inclusion of Veginpicture ... \endpicture as suggested in Section 8.2 was found to be unnecessary in the definition.

An example of logical placement is
\label\#1\#2\#3\#4\{\% \#1=\$x\$ \#2=\$y\$
$\%$ \#3=pin number \#4=label \$ 1 cdots $\$$
used to draw this IC.


Here, each label is placed as


The parameter \#4 can contain any text, including symbols, so that the depth of this sub-box is unknown. Since its depth will define the depth of $\backslash l a b e l$, logical placement is essential to maintain alignment.

Once these concepts were understood, a library of objects and pictograms was developed.

## Tricks

$D$ and o are used to make $D$

Initially, the inversion bubble that converts a buffer to an inverter was constructed using \circulararc, which took a long time to compile and used considerable amounts of memory. It was noticed that a lower case o in courier font is circular, and at 10 pt , of a perfect size to be used as $\backslash$ bubble which can be \put immediately!

- and I are used to make

Infill of shapes proved to be difficult. The diode triangle is constructed by successively \puting on a centerline vertical rules of diminishing height and offset 0.4 pt to each other. The down arrow of the $n p n$ transistor is drawn similarly by $\backslash p l o t t i n g ~ a ~$ series of lines radiating from the tip of the arrow.

Labels of some signals are drawn with a bar over the symbol to indicate that the signals are active low. Given that the symbols are of unknown length, this was solved by

```
\def\actlow#1{\hbox{#1}
\setbox1=\hbox{#1}\kern-\wd1
\raise5.5pt\hbox{%
\vrule height 0.4pt width \wd1
}}
```


## Problems

It must be stressed that problems discussed here relate to our use of $\mathrm{P}_{\mathrm{T}} \mathrm{CIEX}_{\mathrm{E}}$ in trying to create an application for which it was not intended and not to $\mathrm{P}_{\mathrm{I}} \mathrm{CT}_{\mathrm{E}} \mathrm{X}$ itself.

1. \linethickness has no effect on $\mathrm{PICTEX}^{\text {C }}$ macros producing curves so that, for example, the circular outline used to draw the transistor could not be changed.
2. \loop ... \repeat does not seem to work within any of our defined macros which involve use of $\mathrm{P}_{\mathrm{T}} \mathrm{CTEX}$.
3. During development, variable names were chosen which by chance coincided with names used in $\mathrm{P}_{\mathrm{I}} \mathrm{CTEX}_{\mathrm{E}}$. To avoid repetition of the problems this caused, all $\mathrm{CIRTEX}^{2}$ variables now start with a c e.g. \cxone
4. Our initial use of \setbox1 gave very odd results until the valuable advice to always use \newbox was remembered (which produced \box22!).
5. Our attempt at object-orientated development implies deeper and deeper grouping which may contribute to greater use of memory than is necessary.

## Conclusion

The extent of development now allows us to quickly draw diagrams like these,

which can be totally integrated into a TEX document. Indeed, it is now possible to produce diagrams such as that in the Appendix, but this takes 37 kbytes of memory and over 6 minutes to compile on a Macintosh II! The reason for this is simple, that $\mathrm{PTCTEX}_{\mathrm{E}}$ must calculate many of the basic shapes of the objects. A tantalising reference is made on page 389 of the The TEXbook to a font \qc which contains four quarter circles as characters but no hint is given as to its availability. If a font which contains all the basic object shapes which we use could be developed and combined with the computational power implied in the use of \put within $\mathrm{P}_{\mathrm{T}} \mathrm{CT}_{\mathrm{E}} \mathrm{X}$, then as Rogers suggests, we would be able to produce a package capable of easy production of circuit diagrams of suitable quality to be included in text books.

## Bibliography

Norris \& Oakley. 1988. "Electronic Publishing," TEX88 Exeter Proceedings.
Rogers, David. 1988. TUGboat 10(1), April.

## Appendix



